



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 4873

SERIAL NUMBER 10/690,084	FILING DATE 10/20/2003  RULE	CLASS 438	GROUP ART UNIT 1765	ATTORNEY DOCKET NO. NOVLP068/NVLS- 000818
-----------------------------	---------------------------------------	--------------	------------------------	--

APPLICANTS

Daniel A. Koos, Chandler, AZ;

Steven T. Mayer, Lake Oswego, OR;

Heung L. Park, Wilsonville, OR; Timothy Patrick Cleary, Portland, OR;

Thomas Mountsier, San Jose, CA;

\*\* CONTINUING DATA \*\*\*\*\* NONE ✓

\*\* FOREIGN APPLICATIONS \*\*\*\*\* NONE ✓

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 03/12/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature: <i>[Signature]</i> Initials: <i>LV</i>	AZ	3	47	3

ADDRESS

022434

BEYER WEAVER & THOMAS LLP

P.O. BOX 70250

OAKLAND, CA

94612-0250

TITLE

Method for fabrication of semiconductor interconnect structure with reduced capacitance, leakage current, and improved breakdown voltage

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
------------	--	--